

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of)
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SHIRAMIZU et al.)
)
Application Number: To be Assigned)
)
Filed: Concurrently Herewith)
)
For: RECEIVER CIRCUIT AND TRANSMITTER CIRCUIT)
)
ATTORNEY DOCKET NO. NITT.0178)

**Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231**

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97, this Information Disclosure Statement is submitted in the above-identified patent application. A listing of documents to be published on the face of any patent granted from this application is submitted herewith on Form PTO-1449. Any other documents or information submitted for consideration by the Examiner are listed in this paper. A copy of each U.S. and foreign patent, or each publication or portion thereof listed or herein identified, submitted herewith.

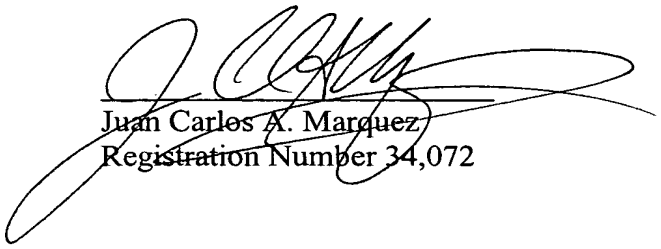
This Information Disclosure Statement is submitted with the initial filing of the application. Accordingly, no fee is due or payable at this time.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

Please charge any additional fees or credit any overpayments in connection with this paper to Deposit Account No. 08-1480.

Respectfully submitted,

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Form PTO 1449 U.S. Department of Commerce Patent and Trademark Office Information Disclosure Statement by Applicant	ATTY. DOCKET NUMBER NITT.0178	SERIAL NUMBER To be Assigned
	APPLICANT SHIRAMIZU et al.	
	FILING DATE Concurrently Herewith	GROUP

U.S. Patent Documents

Examiner Initial		DOCUMENT NUMBER	DATE	NAME	CLA SS	SUBC CLASS	FILING DATE

Foreign Patent Documents

Examiner Initial		DOCUMENT NUMBER	FILING DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION	
							YES	NO

Other Documents (Including Author, Title, Date Pertinent Pages, Etc.)

		H. M. Rein, "Si and SiGe Bipolar ICs for 10 to 40 Gb/s Optical-Fiber TDM Links", Internatinoal Journal of High Speed Electronics and Systems, Vol. 9, No. 2 (1998), published by World Scientific Publishing Company, pp. 347-383
		Zhihao H. Lao et al., "A 12 Gb/s Si Bipolar 4:1-Multiplexer IC for SDH Systems", IEEE Journal of Solid-State Circuits, Vol. 30, No. 2, February 1995, pp. 129-132
EXAMINER		DATE CONSIDERED
<i>EXAMINER: Initial if citation is considered, whether or not citation is in conformance with MPEP 609; draw a line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant</i>		